



RCA INSTITUTES, INC.

A SUBSIDIARY OF RADIO CORPORATION OF AMERICA

BRADFORD DAGGETT
DIRECTOR
CUSTOM EDUCATION

RCA Institutes Announces an Important Seminar --
DIGITAL ELECTRONICS --
To Be Presented in New York City, New York

DIGITAL ELECTRONICS is an intensive five-day seminar on the applications of monolithic and hybrid logic devices. This important program, now in its second edition, is directed to Engineers and other technical personnel who must specify, evaluate, or utilize integrated digital circuits.

The place: The Belmont Plaza Hotel
49th St. at Lexington Avenue
New York, New York

The dates: August 7 through August 11, 1967
9:00 a.m. to 4:30 p.m. daily

We urge you to register promptly, since attendance will be limited. A folder covering the seminar as well as a registration form is enclosed. Hotel reservations should be made directly with the hotel, phone (212) PL 5-1200.

Please call or write us, should you have any questions.

Sincerely,

Bradford Daggett

SEMINAR INFORMATION

STAFF

Bradford Daggett, Director
Matthew Mahoney, Manager, Development
Charles Saville, Manager, Research
Alfred Peticolas, Dean
Mario Laguzzi, Member of Technical Staff
Robert Lindskog, Member of Technical Staff
Douglas Kenney, Member of Technical Staff

REGISTRATION INFORMATION

Seminars will be limited in size, and registrations will be accepted in the order received. Substitution of applicants may be made at any time. Cancellation of registration will be honored and fee refunded provided notification is received no later than one week prior to the seminar. Registration fee includes attendance, luncheon, coffee break, and a complete package of reference material. Reference material will be issued at the opening session. Telephone answering service is provided during seminar hours.

HOTEL ACCOMMODATIONS

RCA Institutes has reserved a block of rooms in each seminar hotel. Hotel room is not included in the registration fee. Additional hotel information will be forwarded with your confirmed reservation. Please arrange room accommodations directly with the hotel, with reference to RCA Institutes' Seminar.

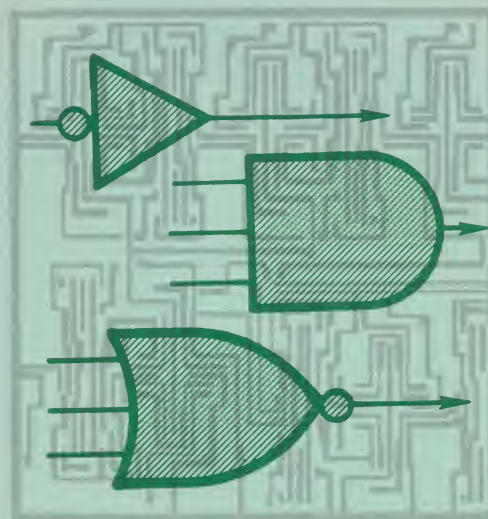
SEMINARS IN RELATED TOPICS

Logic Design Integrated Circuits
Digital Communications Numerical Control
 Digital Systems Engineering



RCA INSTITUTES, Inc.

A Subsidiary of Radio Corporation of America
School of Custom Educational Programs
49 West 45th Street
New York, N. Y. 10036
Area Code 212, 247-5700



RCA INSTITUTES
Announces
A Five-Day Seminar—

DIGITAL ELECTRONICS

Digital Electronics

INTRODUCTION

DIGITAL ELECTRONICS is one of a series of intensive, five-day seminars presented by RCA Institutes. It is specifically directed to the engineer who must convert a digital system diagram into an electronic reality. It provides the knowledge required to utilize, evaluate, or specify integrated logic circuits.

SCOPE

In this seminar, the presentation is given in four areas:

1. The important device parameters are defined, measured, and evaluated.
2. Existing integral digital circuit types are analyzed as to behavior and capabilities.
3. Methods of interfacing with maximum reliability are given.
4. Techniques of "force-fitting" available packages into specialized applications are developed.

PREREQUISITES

The minimum requirements are a baccalaureate degree in engineering or physics, or the equivalent in experience. It will be assumed that each participant knows A.C. circuits and semiconductor theory. Since logic design techniques are not employed in this seminar, a background in Logic is not essential.

PRESENTATION

Members of the School of Custom Educational Programs will present this program of concentrated study using a unique cross-fire teaching technique designed to motivate the participant. Classwork and homework assignments reinforce each day's lecture.

This seminar has been developed through extensive field research by the School of Custom Educational Programs in consultation with RCA Institutes' Board of Technical Advisers, representing various technical, research and educational activities of the Radio Corporation of America and its subsidiaries.

SEMINAR OUTLINE

FIRST DAY

SPECIFICATIONS AND MEASUREMENT

Introduction and orientation; digital circuit specifications; typical measurements; Logic symbology and standards; gates; function packages; RS and JK flip-flops, comparators, one-shots, Schmitt triggers.

SECOND DAY

INTEGRATED LOGIC FORMS

The three-part device concept; transfer characteristics; input sections; AND/OR logic, NAND/NOR types; RTL, DTL, TTL, CML, ECL, HTL circuits; output stages; fan-out; current sinking and sourcing.

THIRD DAY

NOISE IMMUNITY AND INTERFACING

The folded transfer graph; critical voltage parameters; 1-stage, n-stage, and infinite-stage noise propagation; multiple noise sources; supply ripple; transients; choosing optimal circuits; interfacing, device matching, and wiring.

FOURTH DAY

STANDARD LOGIC DEVICES

Silicon technology; integrated circuit capabilities and limitations; packaging; pin-limited design; heat dissipation; design and operation of integrated circuit flip-flops, comparators, adders, matrices, and indicator drivers.

FIFTH DAY

LARGE SCALE INTEGRATION

Functions, arrays, and subsystems; economic considerations of LSI; scratch-pad memory; matrices; arithmetic functions; counters; registers; MOS and junction field-effect circuits; MOS subsystems.

FUTURE SEMINAR ANNOUNCEMENTS

Gentlemen:

Please place the following names
on your mailing list for future seminar announcements:

name _____ title _____

name _____ title _____

name _____ title _____

firm's name _____

address _____ city _____

state _____ zip code _____

Postage
Will be Paid
by
Addressee

No
Postage Stamp
Necessary
If Mailed in the
United States

B U S I N E S S R E P L Y M A I L

FIRST CLASS PERMIT NO. 10662, NEW YORK, N. Y.

RCA INSTITUTES, Inc.
49 West 45th Street
New York, N. Y. 10036

CUSTOM EDUCATIONAL PROGRAMS